Fig. 1

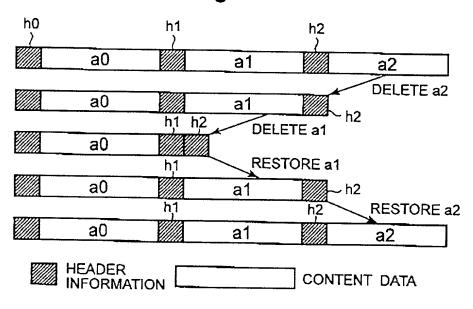


Fig. 2

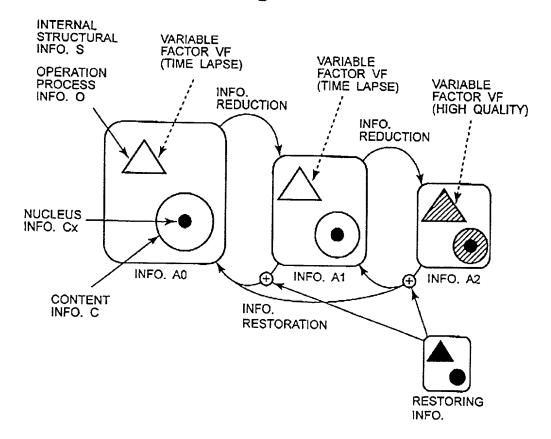


Fig. 3

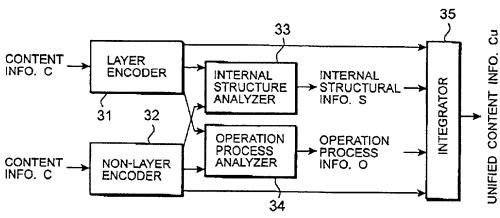


Fig. 4

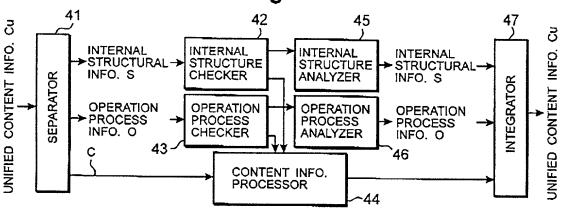


Fig. 5

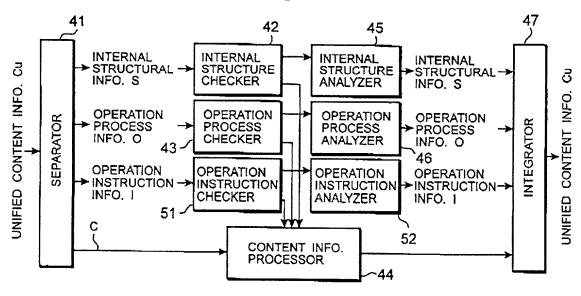


Fig. 6

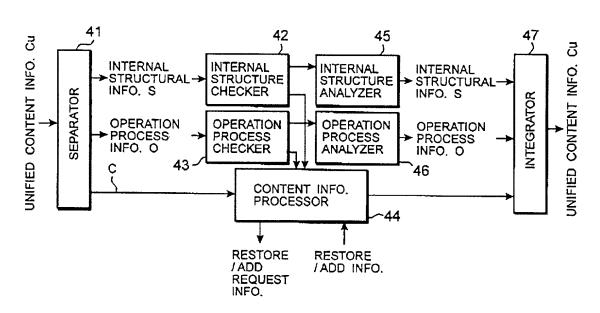


Fig. 7 41 47 42 45 INTERNAL STRUCTURAL → INFO. S INTERNAL INTERNAL INTERNAL  $\overline{c}$ STRUCTURE CHECKER STRUCTURE ANALYZER STRUCTURAL. ರ INFO. S UNIFIED CONTENT INFO. UNIFIED CONTENT INFO. → PROCESS OPERATION PROCESS OPERATION PROCESS INFO. O **OPERATION** SEPARATOR INTEGRATOR PROCESS ANALYZER INFO. O CHECKER 46 OPERATION ►INSTRUCTION OPERATION **OPERATION OPERATION** INSTRUCTION ANALYZER NSTRUCTION→ INSTRUCTION INFO. I CHECKER C 52 CONTENT INFO. **PROCESSOR** 44 RESTORE RESTORE /ADD /ADD INFO. REQUEST INFO.

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Fig. 8

MEMORY

BS

INPUT DEVICE

INPUT OUTPUT INTERFACE

EXTERNAL MEMORY
DEVICE

82

Fig. 9

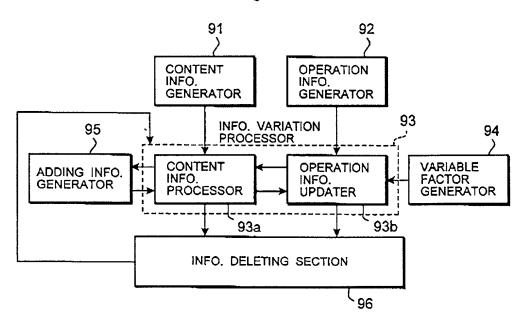


Fig. 10

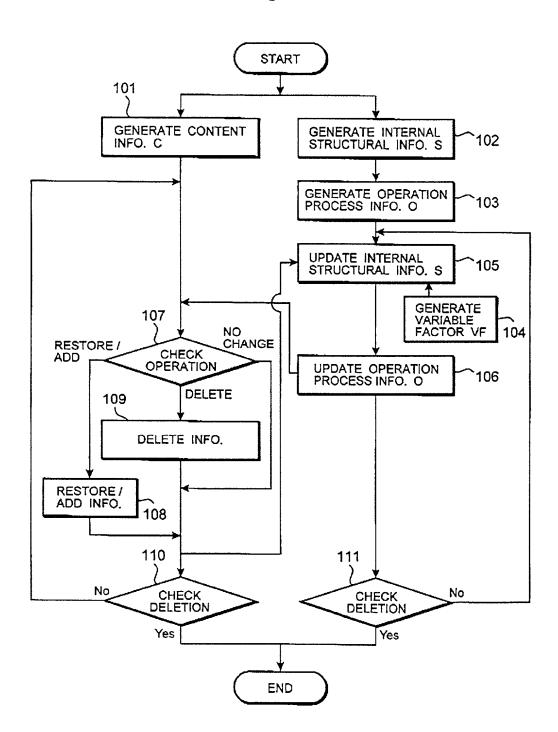


Fig. 11

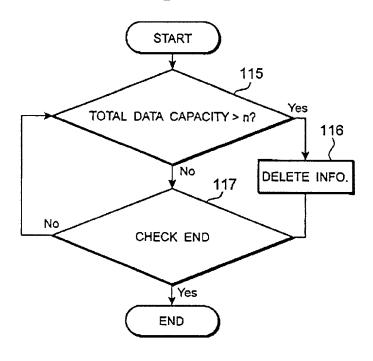


Fig. 12

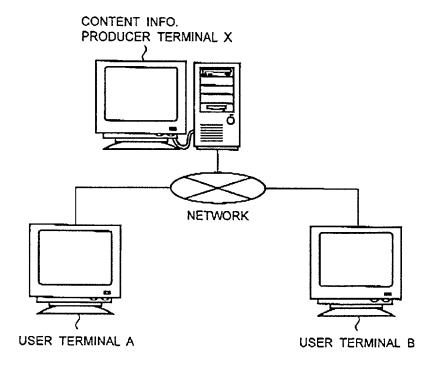


Fig. 13

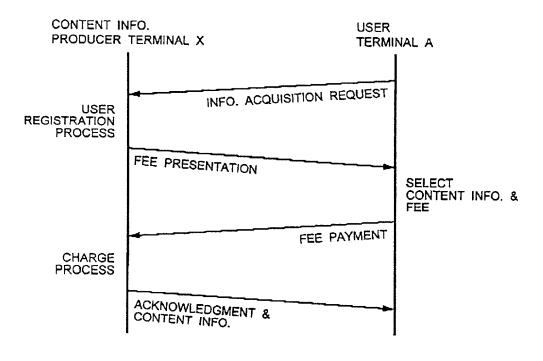


Fig. 14

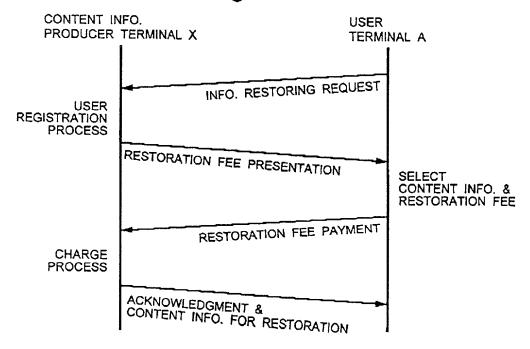


Fig. 15 151 152 CONTENT REQUEST REQUEST RETRANSMISSION REQUEST INFO. REQUEST **ANALYZER** CHECKER INFO. USER CONFIRMATION REGISTRATION PROCESSOR INFO. 153 **PAYMENT PAYMENT PAYMENT** RETRANSMISSION INFO. INFO. INFO. REQUEST INFO. **ANALYZER** CHECKER CONTENT INFO. CHARGE **PROCESSOR** 156 154 155

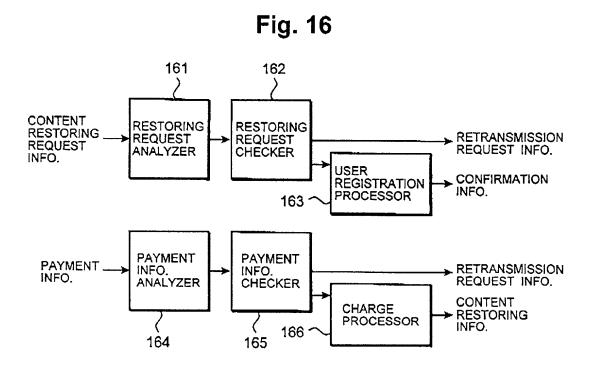


Fig. 17

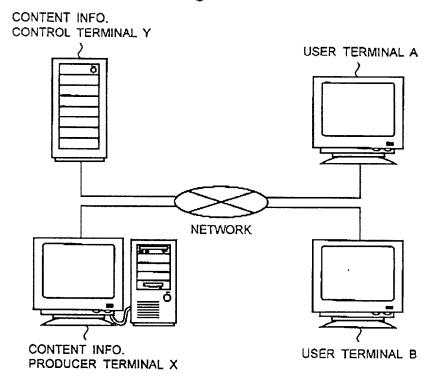


Fig. 18

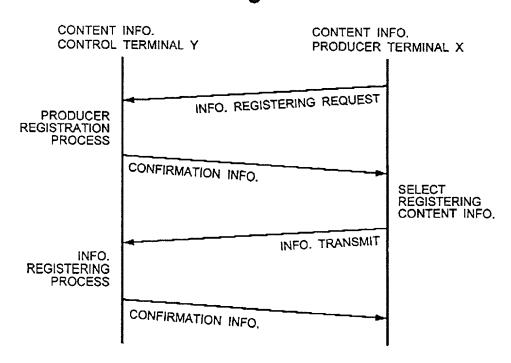


Fig. 19 191 192 CONTENT REGISTRATION REGISTRATION REQUEST ANALYZER REGISTRATION → REQUEST RETRANSMISSION REQUEST INFO. REQUEST INFO. CHECKER **PRODUCER** REGISTRATION PROCESSOR **►** CONFIRMATION INFO. 193 CONTENT REGISTRATION CHECKER CONTENT -CONTENT REGISTRATION ANALYZER ➤ RETRANSMISSION INFO. REQUEST INFO. CONTENT REGISTRATION CONTENT PROCESSOR 194 196 195

Fig. 20

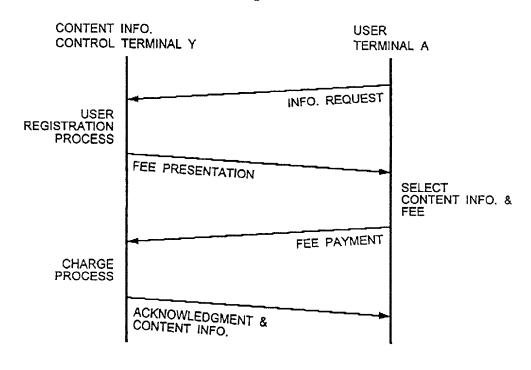


Fig. 21

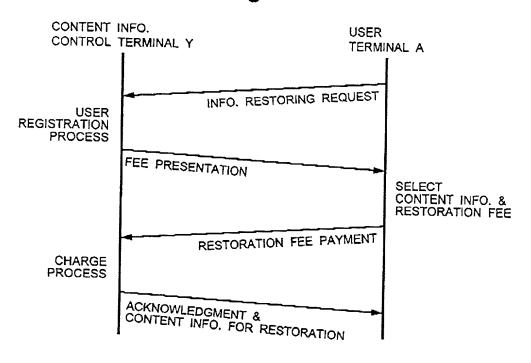


Fig. 22

